

09/530549

	Subclass
	Class
	ISSUE CLASSIFICATION

FILED UNDER 35 U.S.C. 371

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

SCANNED

10/03

Q.A.

PATENT DATE

APPLICATION NO. 09/530549	CONT/PRIOR D F	CLASS 700	SUBCLASS 703	ART UNIT 2763	EXAMINER JONES
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APPLICANTS
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TITLE
METHOD AND DEVICE FOR SYSTEM SIMULATION OF MICROCONTROLLERS/MICROPROCESSORS AND CORRESPONDING PERIPHERAL MODULESPTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

 Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
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